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Thomsen

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(54) **ELECTRONIC CIRCUIT AND METHOD FOR STORING CONFIGURATION AND CALIBRATION INFORMATION IN A NON-VOLATILE MEMORY ARRAY**

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(58) **Field of Search** 365/222, 185.08, 365/189.02, 230.08

(56) **References Cited**

U.S. PATENT DOCUMENTS

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(57) **ABSTRACT**

An electronic circuit includes a non-volatile memory that has several memory cells. The output of the non-volatile memory is connected to the inputs of several latches. Each latch has an output adapted to be read independently. A refresh circuit is connected to address lines of the non-volatile memory and to the write pins of each of the latches. The data contained in the memory cells of the non-volatile memory is represented by the data contained in the latches.

22 Claims, 3 Drawing Sheets

