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[54] **INTEGRATED CIRCUIT PINS CONFIGURABLE AS A CLOCK INPUT PIN AND AS A DIGITAL I/O PIN OR AS A DEVICE RESET PIN AND AS A DIGITAL I/O PIN AND METHOD THEREFOR**

[75] Inventors: **Richard L. Hull**; **Ryan Scott Ellison**, both of Chandler, Ariz.

[73] Assignee: **Microchip Technology Incorporated**, Chandler, Ariz.

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[58] **Field of Search** 326/37–41, 46–47

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Primary Examiner—David R. Hudspeth

Attorney, Agent, or Firm—Harry M. Weiss; Jeffrey D. Moy; Harry M. Weiss & Associates, P.C.

[57] **ABSTRACT**

The present invention relates to a configurable IC device pin. The IC device pin may be a device clock input pin or a digital I/O pin in one embodiment of the present invention, or a reset pin or a digital I/O pin in another embodiment of the present invention. Both embodiments of the present invention use a memory device to store information to configure the IC device pin. Input/Output logic is also used in both embodiments in order to transfer data to and from the IC device pin when the IC device pin is configured as a digital I/O pin.

24 Claims, 3 Drawing Sheets

