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(54) **VECTOR FETCH BUS ERROR HANDLING**

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CPC ***G06F 11/0745*** (2013.01); ***G06F 9/30101*** (2013.01)

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(57) **ABSTRACT**

A computer system includes a non-transitory computer-readable memory to store (a) a vector table including an exception vector pointing to an exception handler and (b) a vector fail address of a vector fetch bus error handler, and a processor to identify an exception, initiate an exception vector fetch in response to the identified exception to read the exception vector from the vector table, identify a vector fetch bus error associated with the exception vector fetch, access the vector fail address of the vector fetch bus error handler in response to the vector fetch bus error, and execute the vector fetch bus error handler.

17 Claims, 4 Drawing Sheets