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(54) **SYSTEM-ON-A-CHIP (SOC) DEVICE WITH
SECONDARY PROCESSOR FOR COLLECTION
OF EXTERNAL DEVICE DATA**

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(57) **ABSTRACT**

A system-on-a-chip (SoC) device includes a primary processor, a primary memory accessible by the primary processor, a secondary processor, a secondary memory, and a communication interface. The secondary processor is configured to acquire external device data from an external device connected to the communication interface, store the acquired external device data in the secondary memory, and transmit the external device data from the secondary memory to the primary memory without interrupting the primary processor.

Related U.S. Application Data

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